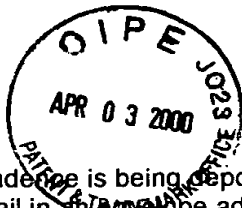


Docket No.: GR 98 F 8041



G P2811

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

By: Matthew Nelt

Date: MARCH 29, 2000

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Thomas Huttner et al.
Applic. No. : 09/313,424
Filed : May 17, 1999
Title : SOI Semiconductor Configuration And Method Of
Fabricating The Same
Examiner : Shouxiang Hu - Art Unit: 2811

#11
Election
FJONES
4-5-00

R E S P O N S E

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated March 1, 2000, the following remarks are made:

In deference to the restriction requirement on pages 2 and 3 of the above-identified Office Action, applicants elect group II, claims 7-15, for prosecution at this time.

Applicants reserve the right to file a divisional application including the device claims of group I at a later date.

In view of the foregoing, the early issuance of an Action on the merits of claims 7-15 is solicited.

Respectfully submitted,

Matthew Nelt Reg. No. 37006

For Applicants

Date: March 29, 2000

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101
/bmb

RECEIVED
APR - 4 2000
TC 2800 MAIL ROOM